

JPW



Docket No.: 060188-0745

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Takaki YOSHIDA : Confirmation Number: 3277
Application No.: 10/751,523 : Group Art Unit: 2825
Filed: January 06, 2004 : Allowed: January 26, 2007
Examiner: KIK, Phallaka

For: ERROR PORTION DETECTING METHOD AND LAYOUT METHOD FOR
SEMICONDUCTOR INTEGRATED CIRCUIT

AMENDMENT UNDER 37 CFR 1.312

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

We are in receipt of the Notice of Allowance dated January 26, 2007, issued in
connection with the above-identified application.

Please amend the claims as presented below.